

ASIC



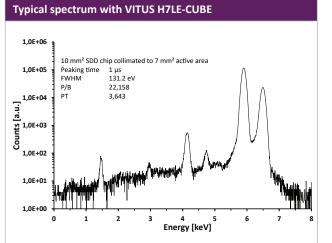
## VITUS H7LE

o 7 mm² collimate

### H7LE VITUS

### X-ray Silicon Drift Detector

- Ultra-low-capacitance ASIC (CUBE) as first amplification stage
- Excellent energy resolution at very short peaking times down to 0.1 μs
- Sensitive down to Boron Kα at 185 eV
- Very high count rate capability up to 2 Mcps
- Operable at up to +50 °C heat sink temperature with outstanding performance
- Best in class peak to background > 10,000 typ.
- Very short signal rise times
- High cooling performance of built-in TEC
- Fully RoHS compatible

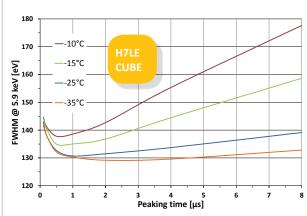


# Guaranteed Specifications First stage amplification

Gmb

First stage amplification	ASIC		
Energy resolution	≤ 129 eV		
Peak to background	> 10,000		
Peak to tail	> 2,000		
Optimal peaking time at max. cooling	1 µs		
Absorption depth Si	450 μm		
Peak shift stability up to 100 kcps	< 1 eV		
Max. input count cate	2,000 kcps		
Window	AP3.3 polymer		
Cooling performance	ΔT > 65 K		
On-chip collimator	Pd		
Ordering code V000-C7	V000-C7T0-H007-PD3E		

#### FWHM vs. Peaking Time at different chip temperatures



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## H7LE VITUS

	Ø14					Typical SDD parameters	Voltages	Currents
-			PI	PIN ASSIGNMENT bottom view		Ring 1 (R1)	-20 V ± 5 V	10 μΑ
2,4			F	Peltier- RX Peltier+		Ring X (RX)	-130 V ± 20 V	10 μΑ
30				Temp+ $( \odot ( \bigcirc ) \odot )$ PR	Deset	Back	-60 V ± 5 V	<1 nA
		9,4	4 Temp+ ( R1		Reset PRE_OUT V_SSS	Peltier element (low energy window)	4.0 V	1000 mA max.
		∞ 				Temperature monitor	10 kΩ *(application not	ermistor @ 25 °C e with linearization available)
	M4					Output signal	ramped	reset type
┝╼┈╞┥						Output gain	1.6 mV/l	keV ± 20 %

Product	Description
VIAMP SDD PLUS PREAMPLIFIER MODULE	The <u>VI</u> TUS <u>Amp</u> lifier is a combination of a VITUS SDD and a low- noise preamplifier circuit, encapsulated in a compact aluminum housing, functioning as an appropriate heat sink. A standardized FFC flex cable connector is integrated. Customized versions for your individual application are also available on request. The VIAMP module can be equipped with all VITUS SDDs from H7 to H50.
AXAS-A ANALOG SDD SYSTEM	The <b>AXAS-A</b> is an <u>a</u> nalogue, vacuum-compatible spectroscopic detector system with regulated power supplies, TEC controller and low-noise preamplifier. This detector system can be equipped with all VITUS SDDs from H7 to H50. The standard finger length is 100 mm, other lengths from 35 to 200 mm are available on request.
AXAS-D DIGITAL SDD SYSTEM	The <b>AXAS-D</b> is a completely <u>d</u> igital, vacuum-compatible spectroscopic detector system. The AXAS-D consists of regulated power supplies, TEC controller, low-noise preamplifier and features, in addition to the AXAS-A, the KETEK digital pulse processor DPP2. This detector system can be equipped with all VITUS SDDs from H7 to H50. The standard finger length is 100 mm, other lengths from 35 to 200 mm are available on request.
EMLCOL CLIP-ON MULTILAYER COLLIMATOR	For all VITUS SDDs up to the H50 a specially designed clip-on protection cap is available. It provides an additional external multilayer collimator to prevent the SDD cap's material excitation (stainless steel). In addition, it further improves P/B and P/T of the detector by preventing the undershooting of the internal multilayer collimator (setup geometry dependent). The EMLCOL also provides an additional protection of the VITUS entrance window.

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