

ASIC

≤ 129 eV

> 15,000

> 2,000

1 µs

450 µm

< 1 eV

2,000 kcps

8 µm

ΔT > 75 K

multilayer

V5C2T0-H7-ML1BEV



VITUS H7

0 mm² collimate o 7 mm²

Guaranteed Specifications First stage amplification

Optimal peaking time at max. cooling

Peak shift stability up to 100 kcps

Energy resolution

Peak to tail

Peak to background

Absorption depth Si

Max. input count cate

Cooling performance

On-chip collimator

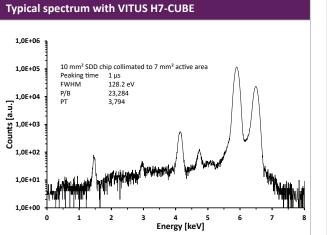
Ordering code

Window Be DuraCoat Plus

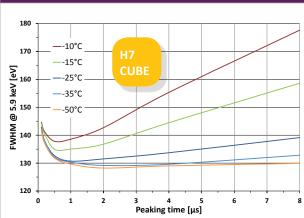
H7 VITUS

X-ray Silicon Drift Detector

- Ultra-low-capacitance ASIC (CUBE) as first amplification stage
- Excellent energy resolution at very short peaking times down to 0.1 μs
- Very high count rate capability up to 2 Mcps
- Operable at up to +50 °C heat sink temperature with outstanding performance
- Best in class peak to background > 20,000 typ.
- Very short signal rise times
- High cooling performance of built-in TEC
- Fully RoHS compatible



UBE FWHM vs. Peaking Time at different chip temperatures



RoHS[™]





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70 77 Rev. 2018-06

. Ø14 .		Typical SDD parameters	Voltages	Currents
	(⊙ ((◯)) ⊙) PRE_OUT	Ring 1 (R1)	-20 V ± 5 V	10 μΑ
<u>5</u> (1)		Ring X (RX)	-130 V ± 20 V	10 μΑ
		Back	-60 V ± 5 V	<1 nA
35 or N A A A A A A A A A A A A A A A A A A		Peltier element (Be window)	3.6 V	700 mA max.
		Temperature monitor	NTC thermistor $10 \text{ k}\Omega @ 25 ^{\circ}\text{C}$ *(application note with linearization circuit available)	
	• Short Pins	Output signal	ramped reset type	
		Output gain	1.6 mV/k	keV ± 20 %

Product	Description
VIAMP SDD PLUS PREAMPLIFIER MODULE	The <u>VI</u> TUS <u>Amp</u> lifier is a combination of a VITUS SDD and a low- noise preamplifier circuit, encapsulated in a compact aluminum housing, functioning as an appropriate heat sink. A standardized FFC flex cable connector is integrated. Customized versions for your individual application are also available on request. The VIAMP module can be equipped with all VITUS SDDs from H7 to H50.
AXAS-A ANALOG SDD SYSTEM	The AXAS-A is an <u>a</u> nalogue, vacuum-compatible spectroscopic detector system with regulated power supplies, TEC controller and low-noise preamplifier. This detector system can be equipped with all VITUS SDDs from H7 to H50. The standard finger length is 100 mm, other lengths from 35 to 200 mm are available on request.
AXAS-D DIGITAL SDD SYSTEM	The AXAS-D is a completely <u>d</u> igital, vacuum-compatible spectroscopic detector system. The AXAS-D consists of regulated power supplies, TEC controller, low-noise preamplifier and features, in addition to the AXAS-A, the KETEK digital pulse processor DPP2. This detector system can be equipped with all VITUS SDDs from H7 to H50. The standard finger length is 100 mm, other lengths from 35 to 200 mm are available on request.
EMLCOL CLIP-ON MULTILAYER COLLIMATOR	For all VITUS SDDs up to the H50 a specially designed clip-on protection cap is available. It provides an additional external multilayer collimator to prevent the SDD cap's material excitation (Nickel). In addition, it further improves P/B and P/T of the detector by preventing the undershooting of the internal multilayer collimator (setup geometry dependent). The EMLCOL also provides an additional protection of the VITUS entrance window.

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