





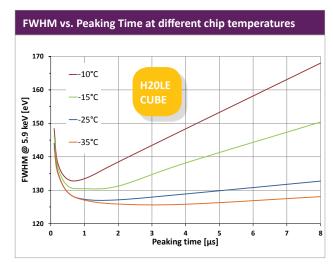


# X-ray Silicon Drift Detector

- Ultra-low-capacitance ASIC (CUBE) as first amplification stage
- Excellent energy resolution at very short peaking times down to 0.1 µs
- Sensitive down to Boron Kα at 185 eV
- Very high count rate capability up to 2 Mcps
- Operable at up to +50 °C heat sink temperature with outstanding performance
- Best in class peak to background > 10,000 typ.
- Very short signal rise times
- High cooling performance of built-in TEC
- Fully RoHS compatible

T	1,0E+06	spectrum with VITUS H20LE-CUBE
Counts [a.u.]	1,0E+05	30 mm² SDD chip collimated to 20 mm² active area Peaking time 1 µs FWHM 130.7 eV
	1,0E+04	P/B 18,147 PT 3,737
	1,0E+03	Λ / V \
	1,0E+02	
	1,0E+01	The state of the s
	1,0E+00	11/10/11/11/11
		0 1 2 3 4 5 6 7 Energy [keV]

Guaranteed Specifications					
First stage amplification	ASIC				
Energy resolution	≤ 129 eV				
Peak to background	> 10,000				
Peak to tail	> 2,000				
Optimal peaking time at max. cooling	1 μs				
Absorption depth Si	450 μm				
Peak shift stability up to 100 kcps	< 1 eV				
Max. input count cate	2,000 kcps				
Window	AP3.3 polymer				
Cooling performance	ΔT > 65 K				
On-chip collimator	Pd				
Ordering code V000-C7	V000-C7T0-H020-PD3E				

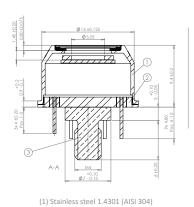




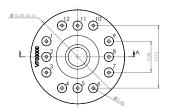








#### PIN ASSIGNMENT bottom view



(2) Temp + (3) R1 (4) Back (5) V\_I/O (6) V\_S

(8) PRE out (9) Reset (10) Peltier + (11) Rx

(12) Peltier -

Description

Typical SDD parameters	Voltages	Currents	
Ring 1 (R1)	-20 V ± 5 V	10 μΑ	
Ring X (RX)	-130 V ± 20 V	10 μΑ	
Back	-60 V ± 5 V	<1 nA	
Peltier element (low energy window)	4.0 V	1000 mA max.	
Temperature monitor	NTC thermistor $10~\text{k}\Omega$ @ $25~\text{°C}$ *(application note with linearization circuit available)		

Output gain

Output signal

1.6 mV/keV ± 20 %

ramped reset type

ъ.	-1	ct	

(2) Kovar (3) Copper

### **VIAMP SDD PLUS PREAMPLIFIER MODULE**



The VITUS Amplifier is a combination of a VITUS SDD and a lownoise preamplifier circuit, encapsulated in a compact aluminum housing, functioning as an appropriate heat sink. A standardized FFC flex cable connector is integrated. Customized versions for your individual application are also available on request. The VIAMP module can be equipped with all VITUS SDDs from H7 to H50.

## **AXAS-A** ANALOG SDD SYSTEM



The AXAS-A is an analogue, vacuum-compatible spectroscopic detector system with regulated power supplies, TEC controller and low-noise preamplifier. This detector system can be equipped with all VITUS SDDs from H7 to H50. The standard finger length is 100 mm, other lengths from 35 to 200 mm are available on request.

#### **AXAS-D** DIGITAL SDD SYSTEM



The **AXAS-D** is a completely **d**igital, vacuum-compatible spectroscopic detector system. The AXAS-D consists of regulated power supplies, TEC controller, low-noise preamplifier and features, in addition to the AXAS-A, the KETEK digital pulse processor DPP2. This detector system can be equipped with all VITUS SDDs from H7 to H50. The standard finger length is 100 mm, other lengths from 35 to 200 mm are available on request.

### **EMLCOL** CLIP-ON MULTILAYER COLLIMATOR



For all VITUS SDDs up to the H50 a specially designed clip-on protection cap is available. It provides an additional external multilayer collimator to prevent the SDD cap's material excitation (stainless steel). In addition, it further improves P/B and P/T of the detector by preventing the undershooting of the internal multilayer collimator (setup geometry dependent). The EMLCOL also provides an additional protection of the VITUS entrance window.

